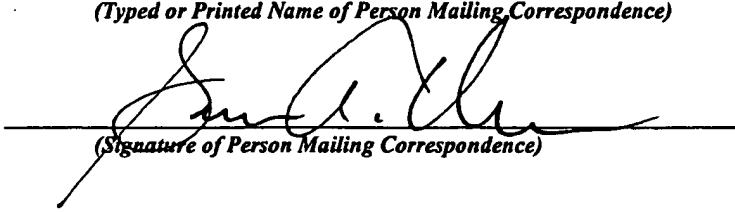




CERTIFICATE OF MAILING BY "EXPRESS MAIL" (37 CFR 1.10) Applicant(s): William K. Lam			Docket No. 16159.089001; P4777
Serial No. 09/990,935	Filing Date November 15, 2001	Examiner	Group Art Unit
Invention: METHOD AND APPARATUS FOR AMORTIZING CRITICAL PATH COMPUTATIONS			
I hereby certify that the following correspondence:			
<u>Information Disclosure Statement along with the required papers</u> (Identify type of correspondence)			
is being deposited with the United States Postal Service "Express Mail Post Office To Addressee" service under 37 CFR 1.10 in an envelope addressed to: The Assistant Commissioner for Patents, Washington, D.C. 20231 on <u>January 22, 2003</u> (Date)			
<u>Grace A. Wise</u> (Typed or Printed Name of Person Mailing Correspondence)  (Signature of Person Mailing Correspondence)			
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S): William K. Lam

ART UNIT:

SERIAL NO.: 09/990,935

EXAMINER:

FILED: November 15, 2001

TITLE: METHOD AND APPARATUS FOR AMORTIZING CRITICAL
PATH COMPUTATIONS

Assistant Commissioner for Patents
Washington, DC 20231

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JAN 28 2003

Technology Center 2100

INFORMATION DISCLOSURE STATEMENT

Pursuant to their duty of good faith and candor as set forth in 37 CFR 1.56(a), Applicants request that the reference(s) cited on attached form PTO-1449 (copies of which are enclosed) be considered by the Examiner in connection with the search of the prior art required by 37 CFR 1.104. This Statement is being filed under one of the following provisions of § 1.97:

[XX] This Statement is being filed under 37 CFR 1.97(b) prior to the latest of: (1) three months from the filing date of this Application; (2) three months after entry of the national stage in the case of an international application; (3) the mailing date of a first Office Action on the merits; or (4) before the mailing of a first Office Action after the filing of a request for continued examination under § 1.114.

[] This Statement is being filed under 37 CFR 1.97(c) after the period specified in § 1.97(b) but prior to the earlier of: (1) a final Office Action under § 1.113; (2) a Notice of Allowance under § 1.113; or (3) an action that otherwise closes prosecution in the application. This Statement is accompanied by either:

[] the fee set forth in § 1.17(p); or
 [] the statement specified in § 1.97(e).

[] This Statement is being filed under 37 CFR 1.97(d) after the period specified in § 1.97(c) but prior to or with payment of the issue fee. This Statement is accompanied by:

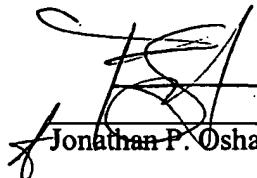
(1) the statement specified in § 1.97(e); and
(2) the fee set forth in § 1.17(p).

Applicants believe no petition is necessary; however, if this belief is incorrect, please consider this paper as such.

Applicants believe there is no fee due for the filing of this paper. If, however, Applicants are in error regarding the fee, the Commissioner is hereby authorized to charge Deposit Account 50-0591 of Rosenthal & Osha L.L.P. under Order No. 16159.087001 for any fees and/or credits during the entire pendency of this application.

Respectfully submitted,

Dated: 1/22/03



#45,079
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<p>SUBSTITUTE FOR FORM 1449 (08-00)</p> <p>U.S. DEPARTMENT OF COMMERCE Patent and Trademark Office</p> <p>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</p> <p>(use as many sheets as necessary)</p>		<i>Complete if Known</i>		
		Application Number	09/990/935	
		Filing Date	November 15, 2001	
		First Named Inventor	William K. Lam	
		Group Art Unit		
Examiner Name				
Sheet	1	of	1	
			Attorney Docket Number	16159.089001; P4777

FOREIGN PATENT DOCUMENTS							
		Foreign Patent Document					
Examiner initials	Cite No.	Office	Number	Kind Code (if known)	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	T**

OTHER DOCUMENTS			
Examiner initials	Cite No.	References	T**
THS	A1	YU-KWONG KWOK and ISHFAQ AHMAD; " <i>Dynamic Critical-Path Scheduling: An Effective Technique for Allocating Task Graphs to Multiprocessors;</i> " May 1996, pp. 506-521, Vol. 7, No. 5, IEEE Transactions On Parallel and Distributed Systems, USA	
THS	A2	TAO YANG and APOSTOLOS GERASOULIS; " <i>DSC: Scheduling Parallel Tasks on an Unbounded Number of Processors;</i> " September 1994, pp. 1-36, IEEE Transactions On Parallel and Distributed Systems, USA	

Examiner Signature		Date Considered	3/1/05
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***EXAMINER:** Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

** Applicant is to place a check mark here if English language is present or an English Translation or Abstract is attached.